

Amendment and Response

Applicant: Ashish Gupta

Serial No.: 10/080,440

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Docket No.: 10019865-1

Title: SYSTEM AND METHOD FOR MEMORY INTERLEAVING USING CELL MAP WITH ENTRY GROUPING FOR HIGHER-WAY INTERLEAVING

IN THE SPECIFICATION

Please insert the following paragraph beginning at page 1, line 5, as follows:

Cross-Reference to Related Applications

A1
This application is related to co-pending U.S. Patent Application Serial No. 10/080,739, filed February 22, 2002 and entitled "System and Method for Input / Output Module Virtualization and Memory Interleaving Using Cell Map", now published as US 2003/0163657 A1.
